REMARKS

Reconsideration and further examination of the subject patent application in light of the present Amendment and Remarks is respectfully requested.

Claims 54-62 are currently pending in the application. Claims 54-62 stand rejected.

Rejection Under 35 U.S.C. §103

Claims 54-62 stand rejected under 35 U.S.C. §103(a) as being obvious over U.S. Pat. No. 5,896,452 to Yip et al. in view of U.S. Pat. No. 6,226,331 to Gambuzza and Allen, "CMOS Analog Circuit Design" published by Saunders College Publishing, 1987, pages 198-200 (hereinafter "Allen). The applicant respectfully traverses these rejections.

The Examiner asserts that "Regarding to claim 54... Yip et al discloses a transmitter... wherein the transmitter presents a high impedance to the transmission line and is in a high impedance state with respect to the normal line impedance of the transmission line when the line driver is not coupled to the transmission line because the transmitter then acts as an open circuit" (Office Action of 2/27/06, page 2). However Yip et al. only discloses two positions of the switch 20, neither of which is an open circuit.

The claimed invention is patentable over Yip because Yip does not involve the context "wherein the transmitter presents a high impedance to the transmission line and is in a high impedance state when the line driver is not coupled to the transmission line, and the transmitter presents a normal impedance to the transmission line and is in a normal impedance state when the line driver is coupled to the transmission line". Yip involves the use of a switch 20. In a first position of the switch 20, a training signal is transmitted from the controller 22 to the hybrid 12.

In a second position of the switch 20, the TRANSMIT PATH is connected to the hybrid 12.

The impedance presented to the hybrid 12 from the YIP controller 22 and YIP TRANSMIT PATH must necessarily be the same. This is necessarily true because transceivers (i.e., the YIP controller 22 and YIP TRANSMIT PATH) must be equal to the characteristic impedance of the hybrid 12 and have a normal impedance. The reasons for this are explained in the specification on page 2, lines 5-10. Since the YIP controller 22 and TRANSMIT PATH present the same impedance, YIP does not meet the limitation recited above.

Similarly, Gambuzza also presents a normal impedance to the transmission line. Again, the reasons for this are explained in the specification on page 2, lines 5-10.

Allen is directed to CMOS analog circuit design in general. Since there is no specific reference to transmission lines, there is no suggestion to combine Allen with Yip et al. or Gambuzza other than the applicant's specification based upon hindsight analysis.

The Examiner Interview

On June 1, 2006, the undersigned attorney for the applicant interviewed Examiner Phu. During the Interview, the undersigned attorney explained the differences between the claimed invention and Yip et al. and the fact that Yip et al. does not provide any teaching or suggestion of a high impedance.

During the Interview (and in the Interview Summary signed by Examiner Phu on 6/1/06), the Examiner agreed that the cited references do not teach "a resistor coupled to the transmission line, the resistor having a high impedance with respect to the normal impedance of the transmission line". As such, the cited combination of references to not meet the claim limitation

"wherein the high speed communication transceiver presents a high impedance to the transmission line with respect to the normal line impedance when the transmitter is not coupled to the transmission line interface, and the high speed communication transceiver presents an impedance to the transmission line that is substantially equal to the normal line impedance when the transmitter is coupled to the transmission line interface."

The Examiner has agreed that the rejections based upon Yip et al., Gambuzza and Allen are improper and should be withdrawn.

Closing Remarks

For the foregoing reasons, applicant submits that the subject application is in condition for allowance and earnestly solicits an early Notice of Allowance. Should the Primary Examiner be of the opinion that a telephone conference would expedite prosecution of the subject application, the Primary Examiner is respectfully requested to call the undersigned at the below-listed number.

The Commissioner is hereby authorized to charge any additional fee which may be required for this application under 37 C.F.R. §§ 1.16-1.18, including but not limited to the issue fee, or credit any overpayment, to Deposit Account No. 23-0920. Should no proper amount be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal, or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 23-0920. A duplicate copy of this sheet(s) is enclosed.

Respectfully submitted,

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By

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